5 Nguyen Gia Thieu, ward 6, district 3, Ho Chi Minh city, Viet Nam Phone: 84 (028) 39302221

http://www.uniquify.com

### **RECRUITMENT NOTICE**

### PHYSICAL DESIGN ENGINEER

# **About Uniquify**

Uniquify, Inc. is a privately held, rapidly growing system-on-chip (SoC) design, integration and manufacturing services supplier, and innovative developer and provider of high-performance adaptive IP. Our "ideas2silicon" services range from spec development and front-end design through physical design to delivery of packaged, tested chips. Uniquify's San Jose headquarters is its primary design center but additional design teams are headquartered in Vietnam, India, Korea, Japan, and China.

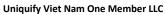
## **Opportunities:**

To join Uniquify Vietnam (UNQVN), Graduates will participate in technical training program to prepare to work as a Physical Design Engineer.

- Front-to-back SOC design in leading edge technology
- Complex SOC design integration and verification.
- Comprehensive Physical Design from RTL to GDS with in-house PerseusTM Design Management System
- Design-For-Test and Multiple Corners Static Timing Analysis to enhance the yield and reliability
- Physical verification and power analysis
- Design Automation (CAD) to enable the consistency and predictable tape out schedule and risk mitigation for complex SOC chip.

### **Job requirements**

- In the last year of undergraduate program in Electronics Engineering or Physics Engineering or with working experience of 1-2 years.
- Strong analytical, logical thinking, debugging skills.
- Good understanding of Fundamentals of Synchronous logic design.
- Good communication skills, positive attitude, and ability to work in a team environment.
- High GPA score (7.0 and above)
- A good command of English.
- Exposure to a scripting languages such as TCL, Perl, and/or C-shell is a plus.
- Prefer to working in Linux environment.





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- Coursework in digital IC design is a plus.
- Prefer to experiencing with Verilog coding and verification.
- Project or industry experience in ASIC (Backend design: DFT, Layout, STA) is a plus.
- Prefer to understand process window & variation in semiconductor design

# How to apply:

Please send a package of your documents to email address <u>unq vn hr@uniquify.com</u> for screening:

- Resume in English
- Transcript
- TOEIC/ IELTS/ TOEFL certificate/ result.